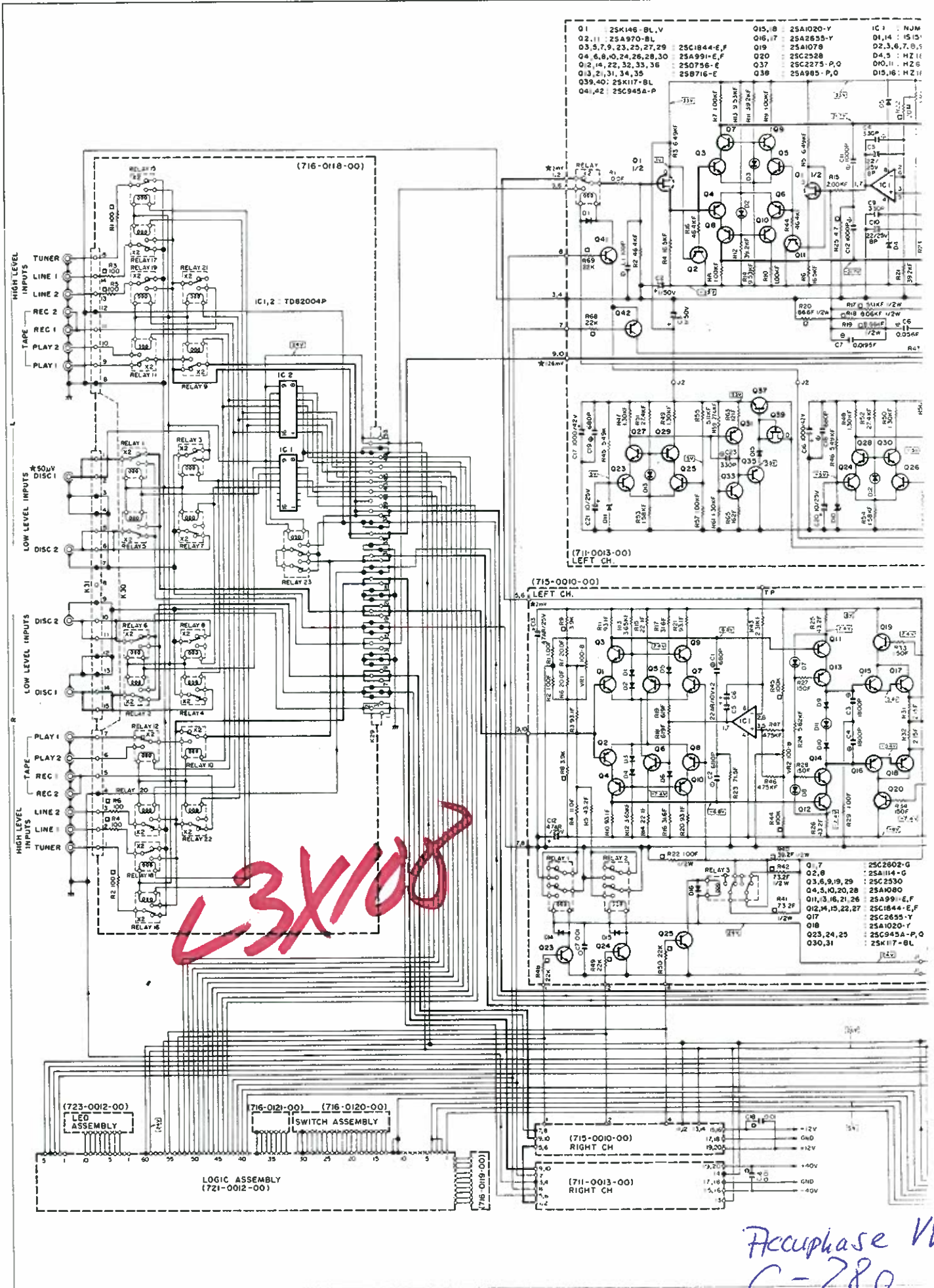


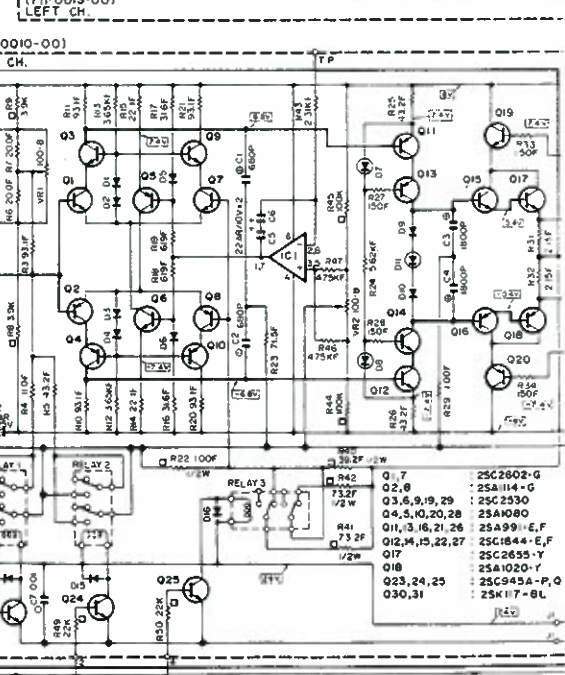
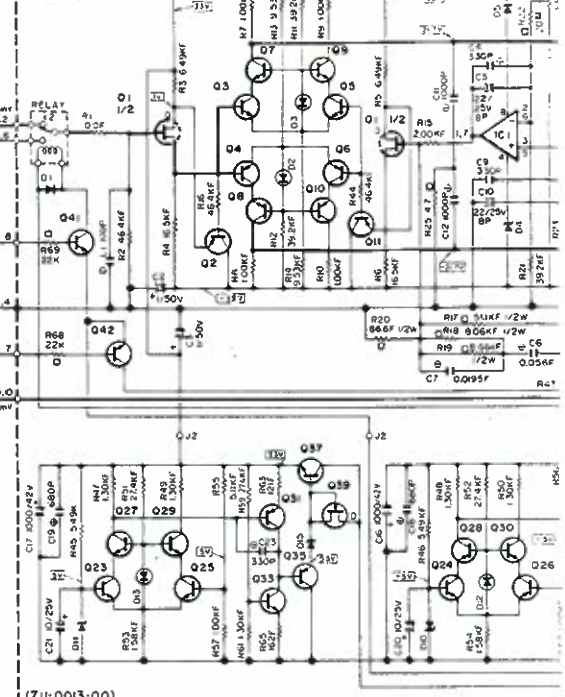
C-280 SCHEMATIC DIAGRAM

NOTES

- 1 The heavy lines on the schematic denote the signal path
- 2 Big spots denote the ground
- 3 The mark of capacitors and resistors on the schematic are
 - CERAMIC CAPACITORS
 - MICA CAPACITORS
 - ⊖ TANTALUM SOLID CAPACITORS
- METALIZED FILM POLYSTYRENE
- METALIZED PAPER
- METALIZED POLYESTER
- WIRE WOUND
- ⊖ SOLID RESISTOR



Q1	25K146-BL-V	Q15,8	25A1020-Y	IC1	716-0118-00
Q2,11	25A970-BL	Q16,17	25A2655-Y	IC2	716-0120-00
Q3,5,7,9,23,25,27,29	25C1844-E,F	Q19	25A1078	D1,4	5D15
Q4,6,8,10,24,26,28,30	25A991-E,F	Q20	25C2528	D2,3,6,7,8,9	5D15
Q12,14,22,32,33,36	25O756-E	Q37	25C2275-P,Q	D4,5	HZ11
Q13,21,31,34,35	25B716-E	Q38	25A985-P,Q	D10,11	HZ6
Q39,40	25K117-BL			D15,16	HZ11
Q41,42	25C945A-P				



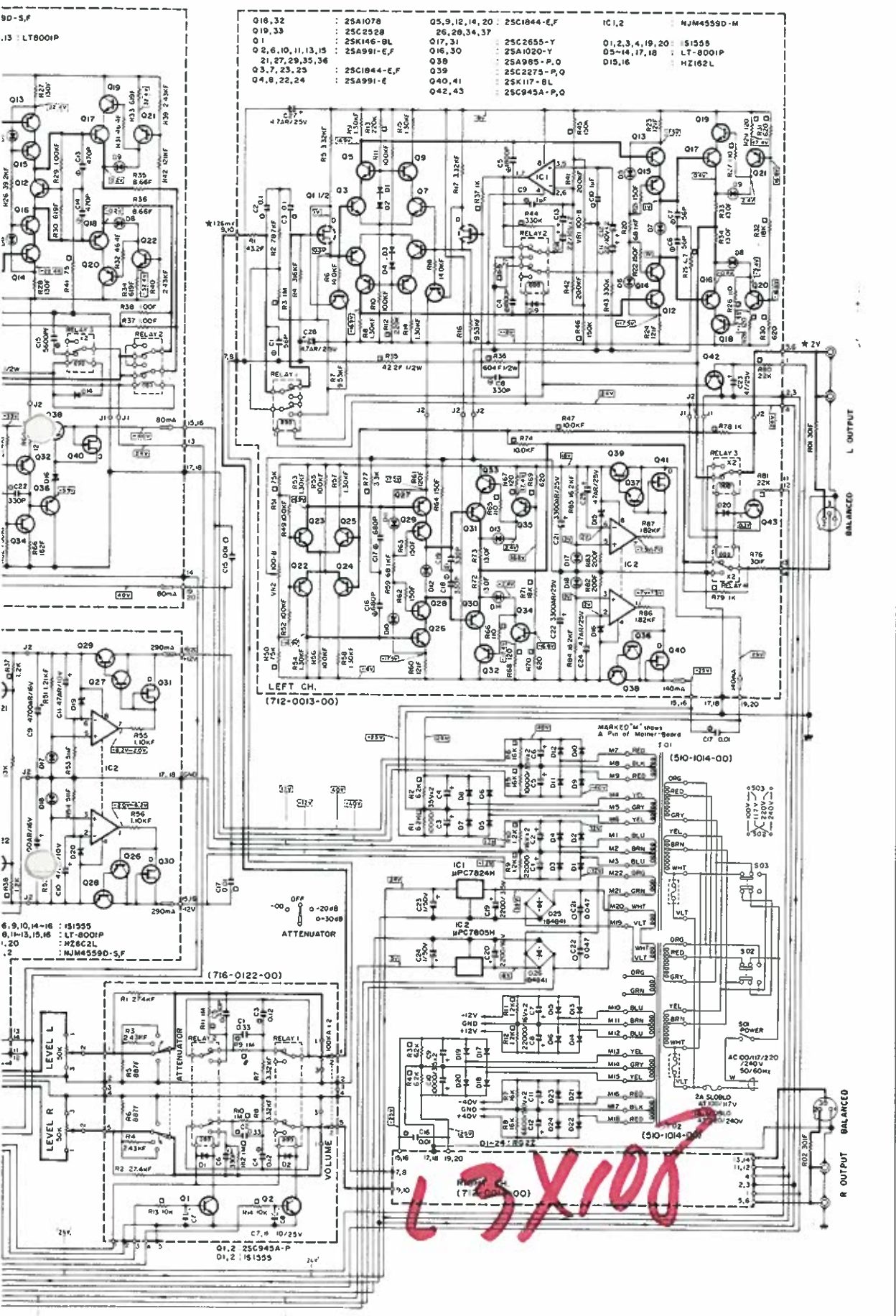
Accuphase VV
C-280

CAPACITORS
 M CAPACITORS
 ESTEL FILM CAPACITORS
 PROPYLENE FILM CAPACITORS
 SISTORS

○ CARBON FILM RESISTORS
 ■ OXIDE METAL FILM RESISTORS
 ▽ METAL PLATE RESISTORS
 ⊞ FUSE RESISTOR

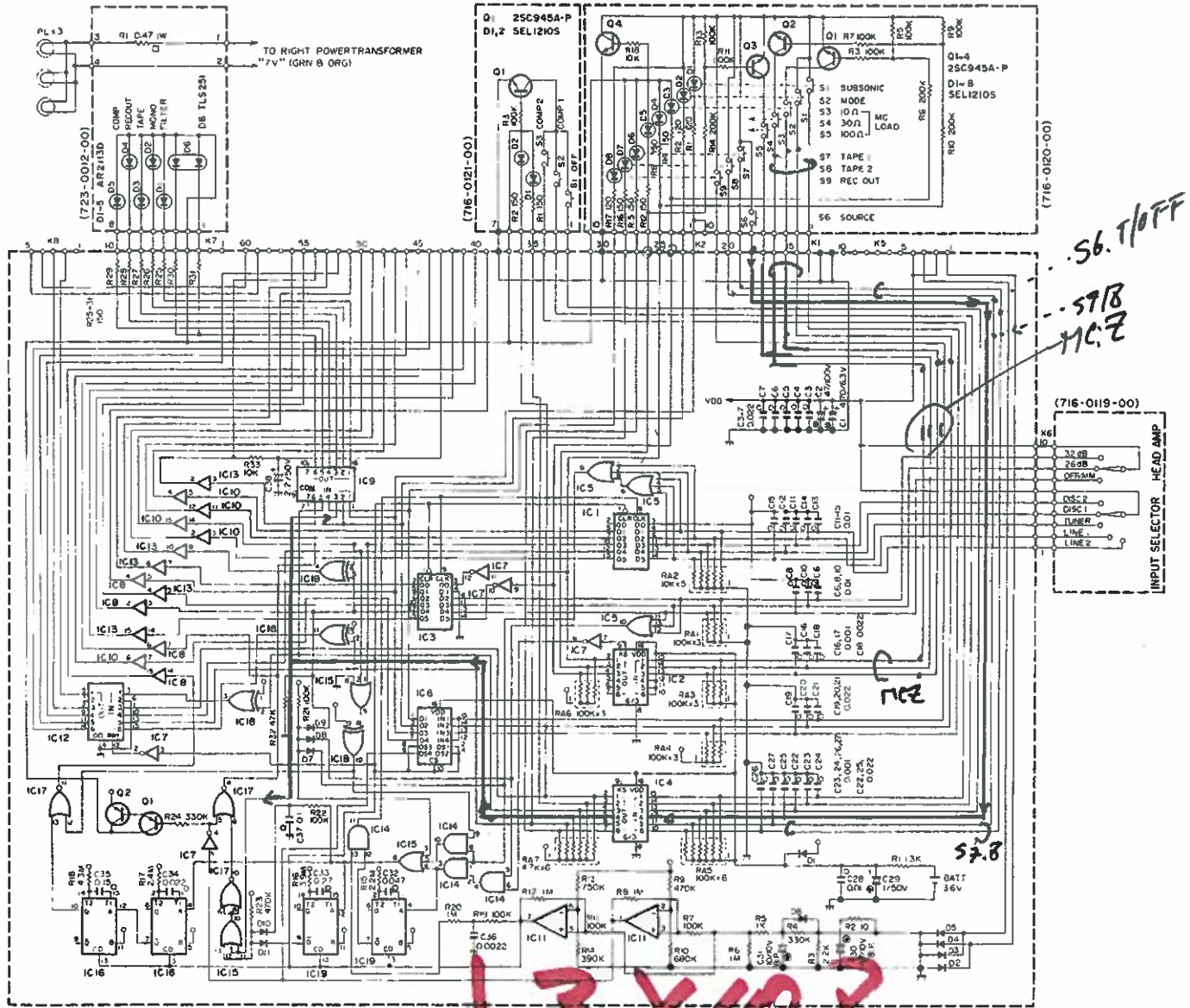
4 VOLTAGE Operating with no input
 * Indicates respective voltage reading when an input of 1kHz
 50.0V is fed to DISC 1 with Head Amp at 26dB position
 5 CURRENT Operating with no input

Unless otherwise specified Capacitors are ELECTROLYTIC types
 Resistors are METAL FILM types 1/4 watt and ± 1% tolerance



SCHEMATIC DIAGRAM

LOGIC CIRCUIT (721-0012-00)



- IC1,3 TC40174BP/HDI4174BP
- IC2,4 TC9135P
- IC5,15 TC4075P/HDI4075BP/μPD4075BC
- IC6 TC9130P
- IC7 TC40498P/HDI4049UBP/
μPD4049UBC
- IC8,10,13 TC40508P/HDI4050BP/μPD4050BC
- IC9 TD62004P/μPA2004C/M54526P
- IC11 TA75902P/μPC324C
- IC12 TC4502BP/HDI4502BP
- IC14 TC4081BP/HDI4081BP/μPD4081BC
- IC16,19 TC45388P/HDI4538BP
- IC17 TC4001BP/HDI4001BP
- IC18 TC4030BP/μPD4030BC

- Q1 2SA733A-P
- Q2 2SA966-Y

- D1-11 1S1555

Fig1

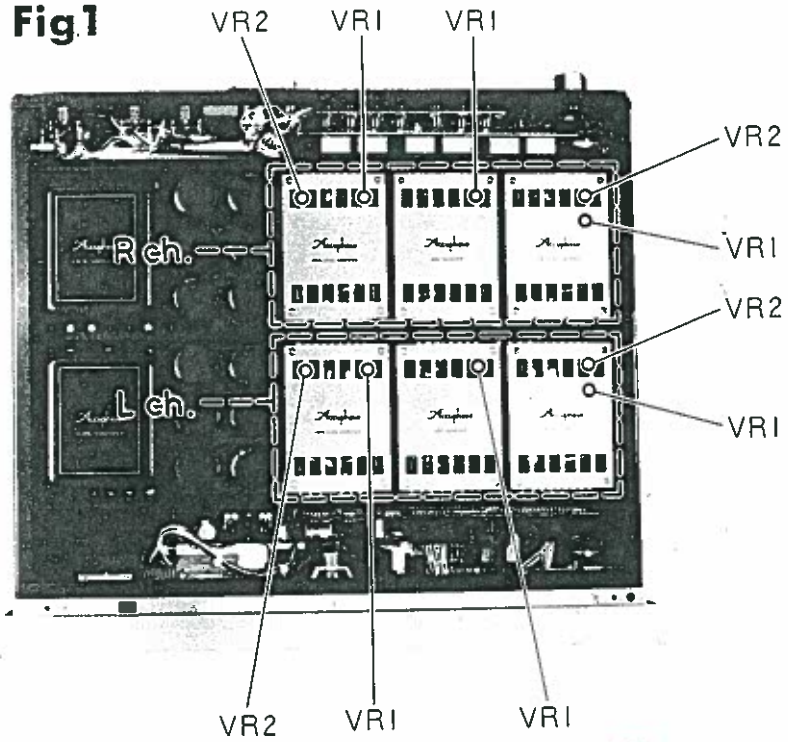
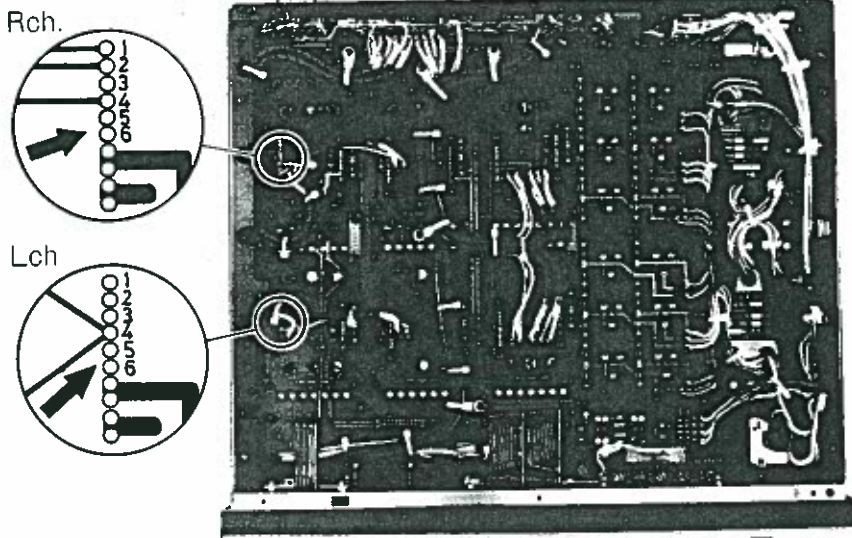


Fig2

L3X108



C280

CIRCUIT ADJUSTMENT (回路調整)

- Internal input resistance of volt-ohm meter should be higher than 10kΩ.
使用テスターは入力抵抗が10kΩ以上のものをお使い下さい。
- When replacing parts, following adjustment/confirmation should be certainly made.
調整は通常は全く不要ですが、基板内の部品を交換した場合、その基板の調整・確認を行って下さい。
- When adjusting OFFSET with V.O. Meter, adjust it carefully to obtain exact "O" reading of the meter.
OFFSET調整はテスタの指針がわずかしか動きませんので、全く動かない程度に調整して下さい。

※Common for Left and Right Channel

STEP ステップ	ADJUST ITEM 調整項目	PROCEDURE 電圧検出		ADJUST 調整箇所	REMARKS 調整・備考
		TEST EQ'PT 検出器	CONNECTING POINT 接続点		
LOW LEVEL AMP ASSEMBLY (715-0010-00) ローレベルアンプAss'y					
1	OFFSET of Input. 入力オフセット	V.O. Meter Set Range to less than DC0.3V テスター DC0.3V以下のレンジ	DISC Input Jack DISC入力端子	VR1	Adjust for "O" reading of V.O. Meter テスターの指示"O"に調整
2	OFFSET of Output. 出力オフセット		Refer fig. 2 No. 5 and 6 of Pattern Land. 2図のコネクタ5、6番	VR2	
DISC EQUALIZER AMP ASSEMBLY (711-0013-00) ディスク イコライザアンプAss'y					
1	OFFSET of Output. 出力オフセット	V.O. Meter Set Range to less than DC0.3V テスターDC0.3V以下のレンジ	REC OUT (REC Switch ON) (RECスイッチをONにする)	VR1	Adjust for "O" reading of V.O. Meter テスターの指示"O"に調整
HIGH LEVEL AMP ASSEMBLY (712-0013-00) ハイレベルアンプAss'y					
1	OFFSET of Output. 出力オフセット	V.O. Meter Set Range to less than DC0.3V テスター DC0.3V以下のレンジ	Output Jack, or No. 2 pin of BALANCED OUT. 出力端子、又はキャノン2番端子	VR1	Adjust for "O" reading of V.O. Meter テスターの指示"O"に調整
2	OFFSET of Inverted Amp Output. 反転アンプ出力オフセット		No. 3 pin of BALANCED OUT. キャノン・コネクタNo. 3端子	VR2	

L3X108